



SNS COLLEGE OF ENGINEERING

Kurumbapalayam (Po), Coimbatore – 641 107

AN AUTONOMOUS INSTITUTION



Academic Year: 2024-2025 (ODD)

23ECB204-DIGITAL ELECTRONICS AND MICROPROCESSOR

UNIT –I

COMBINATIONAL CIRCUITS

PART A

1. Define combinational logic.
2. Explain the design procedure for combinational circuits
3. Define Half adder and full adder
4. What is Decoder
5. Define Encoder?
6. Construct 2:1 multiplexer?
7. Define subtractor?
8. Difference between encoder and decoder
9. Define de-multiplexer?
10. Explain Applications of Multiplexer
11. Define Minterm & Maxterm
12. Explain SOP and POS form
13. Explain Canonical SOP form
14. State De Morgan's theorem
15. Explain demorgan's law
16. What is an essential prime implicant