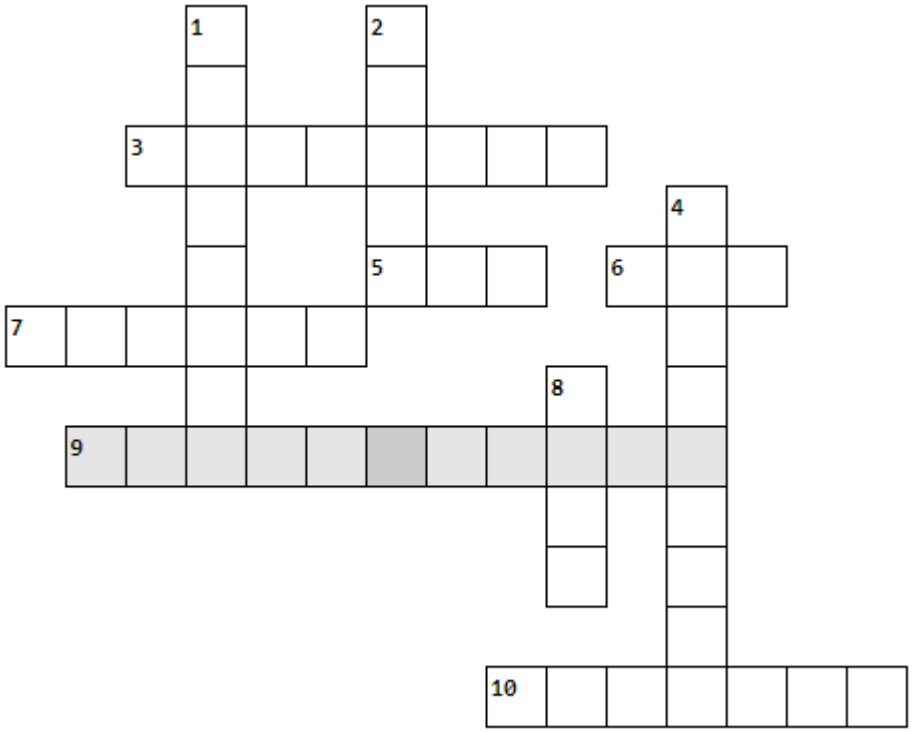
# Digital Electronics Crossword




**Across**

3.1's complement can be easily obtained by using _____

**5.** _____expressions can be implemented using either (1) 2-level AND-OR logic circuits or (2) 2-level NAND logic circuits

**6.** The logical sum of two or more logical product term is called _____

**7.** A three digit decimal number requires _____ for representation in the conventional BCD format

**9.** A Karnaugh map (K-Map) is an abstract form of _____ organized as a matrix of squares

**10.** A product term containing all K variables of the function in either complemented or uncomplemented form is called a _____

**Down**

**1.** Each group of Adjacent Minterms (group size in powers of twos) correspondent to a possible product of a term of the given _____

**2.** Maxterm is the sum of _____ of the corresponding Minterm with its lateral complemented

**4.** The K-Map based Boolen reduction is based on the following Unifying Theorem: A + A'=1.

**8.** The Code where all the successive numbers differ from their preceeding number by single bit is _____