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SNS COLLEGE OF ENGINEERING

Computer organisation and Architecture

UNIT-V

MEMORY SYSTEM MEMORY AND I/O SYSTEMS

- 1. Give the classification of the Optical Media
- 2. What is a Mini Disk?

3. List some applications for WORM.

- 4. What are multifunctional drives
- 5. What are types of technology used in s multifunctional drive?
- 6. What is Migration and Archiving?
- 7. What is the use of High water marks in a cache?
- 8. What are the various cache usage in a LAN -based system?
- 9. What are the multimedia applications which use caches?
- 10. Explain virtual memory technique.
- 11. What are virtual and logical addresses?
- 12. Define translation buffer.
- 13. What is branch delay slot?
- 14. What is optical memory?
- 15. What are static and dynamic memories?
- 18. What are the components of memory management unit?
- 19. Distinguish Between Static RAM and Dynamic RAM? .
- 20. Distiguish between asynchronies DRAM and synchronous RAM.
- 21. What do you mean associative mapping technique?
- 22. What is SCSI?
- 23. What are the two types of latencies associated with storage?
- 24. What are the data management activities involved in a storage?
- 25. What do you mean by Disk Spanning?
- 26. List some objectives for using RAID Systems
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- 27. What are the different levels RAID?
- 28.Two Types of storage devices.
- 29.Explain very briefly about ESDI Hard Drive
- 30. Explain in brief about IDE
- 31. What is SCSI?
- 32. Define the term RELIABILITY
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- **33.Define the term AVAILABLITY:**
- 34. How the interrupt is handled during exception?
- 35. What is IO mapped input output?
- 36.Specify the three types of the DMA transfer techniques?
- **37.** What is an interrupt?
- 38. What are the uses of interrupts?

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39.Define vectored interrupts.

40. Name any three of the standard I/O interface.

41. What is an I/O channel?

42. Why program controlled I/O is unsuitable for high-speed data transfer?

47.what is the function of i/o interface?

48. Name some of the IO devices.

49. What are the steps taken when an interrupt occurs?

50.Define interface.

51.What is programmed I/O?

52. What is DMA?

PART B

1. Define cache memory. Explain the mapping process followed in cache memory. Also

discuss

2. the relative advantages and disadvantages of the mapping techniques used.

3. What is virtual memory? Why is it necessary to implement virtual memory?

Explain the

virtual

4. memory address translation.

5. Draw and explain the various types of secondary storage devices.

6. .List the different types of interrupts. Explain briefly about mask able interrupt.

7. What is DMA? Explain the block diagram of DMA .Also describe how DMA is used to

8. transfer data from peripherals.

9. Expalin input/output processors