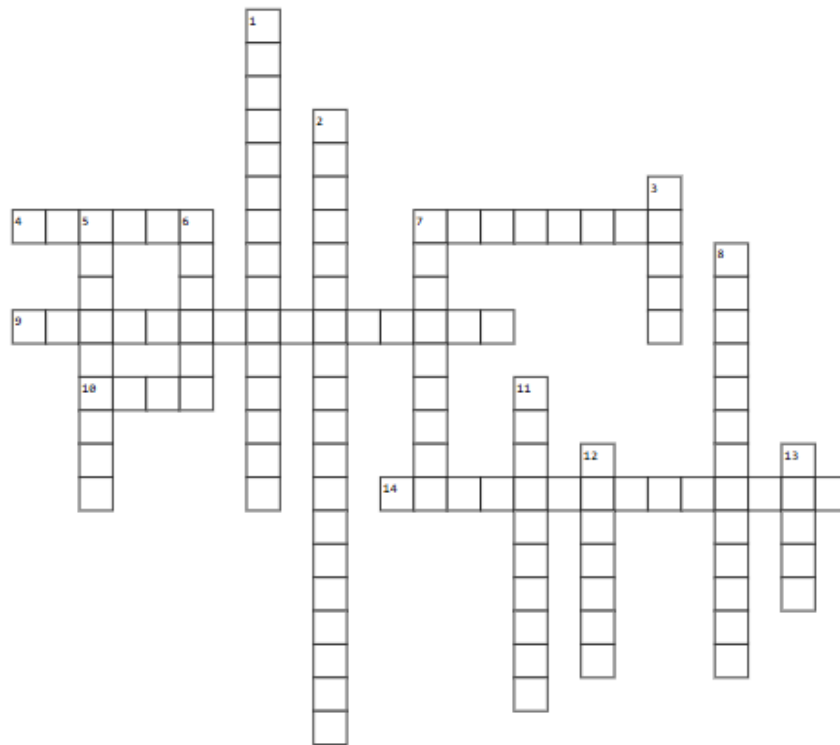


Sequential Circuit-Puzzle



Across

4. If one wants to design a binary counter the preferred type of flip-flop is _____
7. In a NAND based S'-R' latch if S'=1 & R'=1 then the state of the latch is _____
9. One example of the use of an S-R flip flop is as _____
10. How many types of flip-flops are?
14. What does the half circle on the clock input of a J-K flip-flop mean?

Down

1. Flip-flops are _____
2. In JK flip flop same input i.e. at a particular time or during a clock pulse ,the output will oscillate back and forth between 0 and 1 at the end of the clock pulse the value of output Q is uncertain the situation is referred to as?
3. In S-R flip-flop if Q=0 the output is said to be _____
5. Which of the following flip-flops is free from the race around the problem?
6. 0 state the asynchronous input can be used to set the flip-flop to the _____
7. When both inputs of a J-K flip flop cycle the output will _____
8. In a positive edge triggered JK flip flop a low J and low K produces?
11. Which of the following is the universal flip-flop?
12. Both the J-K & the T flip-flop are derived from the basic _____
13. If an active HIGH S-R latch has a 0 on the S input and a 1 on the R input and then the R input goes to 0 the latch will be _____